

## Datasheet – Laser Diode Driver LDD-1137 (0- 75 A / 0- 70 V)



### Support / First steps

Meerstetter Engineering provides technical support for all products and helps you to integrate a product into your solution. Most of your questions should be solved by reading the provided [user manuals](#) of the corresponding product or the [FAQ](#) (frequently asked questions).

For further help or if you have any other questions please do not hesitate to contact us. We are happy to help you. You can contact us by email [support@meerstetter.ch](mailto:support@meerstetter.ch) or by downloading the Meerstetter Engineering [TeamViewer](#) remote support tool.

### Meerstetter's product family compatibility

The Meerstetter LDD and TEC-Family have been developed to work along with each other. They share the same platform bus, communication protocol and hardware architecture. See Table for an Overview over the LDD- and TEC-Families.

<b>LDD-Family</b>		
<a href="#">LDD-1321</a>	0- 1.5 A / 0- 14 V	CW, Add on TEC Controller available
<a href="#">LDD-1301</a>	0- 20 A / 0.5- 45 V	1 ms - CW
<a href="#">LDD-1303</a>	0- 20 A / 1- 120 V	1 ms - CW
<a href="#">LDD-1137</a>	0- 75 A / 0- 70 V	0.5 $\mu$ s - CW, modulated, QCW and pulsed modes
<a href="#">LDD-1124-SV</a>	0- 1.5 A / 0- 15 V	1 $\mu$ s - CW, modulated, QCW and pulsed modes
<a href="#">LDD-1121-SV</a>	0- 15 A / 0- 15 V	1 $\mu$ s - CW, modulated, QCW and pulsed modes
<a href="#">LDD-1125-HV</a>	0- 30A / 0- 27 V	1 $\mu$ s - CW, modulated, QCW and pulsed modes
<b>TEC-Family</b>		
<a href="#">TEC-1092</a>	$\pm$ 1.2 A / $\pm$ 9.6 V	Micro, single channel
<a href="#">TEC-1091</a>	$\pm$ 4 A / $\pm$ 21 V	Small, single channel
<a href="#">TEC-1089-SV</a>	$\pm$ 10 A / $\pm$ 21 V	Medium, single channel
<a href="#">TEC-1162</a>	$\pm$ 5 A / $\pm$ 56 V	Medium-high, single channel
<a href="#">TEC-1090-HV</a>	$\pm$ 16 A / $\pm$ 30 V	Large, single channel
<a href="#">TEC-1163</a>	$\pm$ 25 A / $\pm$ 56 V	Extra-large, single channel
<a href="#">TEC-1161-4A</a>	2 x ( $\pm$ 4 A / $\pm$ 21 V)	Small, dual channels
<a href="#">TEC-1161-10A</a>	2 x ( $\pm$ 10 A / $\pm$ 21 V)	Medium, dual channels
<a href="#">TEC-1122-SV</a>	2 x ( $\pm$ 10 A / $\pm$ 21 V)	Medium, dual channels
<a href="#">TEC-1166</a>	2 x ( $\pm$ 5 A / $\pm$ 56 V)	Medium-high, dual channels
<a href="#">TEC-1123-HV</a>	2 x ( $\pm$ 16 A / $\pm$ 30 V)	Large, dual channels
<a href="#">TEC-1167</a>	2 x ( $\pm$ 25 A / $\pm$ 56 V)	Extra-large, dual channels

**Advanced OEM Laser Diode Driver**  
**with Laser Power Control [PD optional]**



**Description:**

The LDD-1137 is a specialized laser diode driver, able to precision-drive laser diodes in continuous operation. New emerging technologies like eGaN FETs enable improved wave forms and better efficiency. A new processing core enables a faster processor connection to the powerful FPGA, more interfaces, more precise timing, additional functions and much more, while still maintaining a 1.5 mA resolution with much higher current and keeping a very low output ripple.

Equipped with optional light measurement circuitry, the LDD-1137-PD can also be operated as a Laser Power Controller (LPC).

The LDD-1137 offers various safety features, including an input for laser diode temperature monitoring. They are fully digitally controlled; their firmware is upgradeable to offer various communication options and to meet specific customer requirements.

**Features**

**Input Characteristics:**

- DC Input Voltage: 18 to 75 V

**Output Characteristics CW Operation:**

- Voltage: up to 70 V
- Current: up to 75 A (>60 A for a prolonged period may require additional cooling)

**Main Features:**

- Lookup Table with up to 64000 Samples for arbitrary current wave forms
- Error: Ultra-Fast Switch-off for optimal LD protection
- Configuration / Diagnosis: on PC (via USB / RS485 / Ethernet)
- Dimensions (L x W x H): 118 mm x 171 mm x 48 mm
- Efficiency: >96 % (@ 50 % Duty cycle)
- Cooling: over Base Plate

**Power Stage:**

- Output Current: 0-75 A, ~0.2 % Ripple  
Parallel operation of multiple devices on request
- Temperature Coefficient, Typ: 20 ppm/K
- CW Current Resolution: 1.5 mA

**Interfaces:**

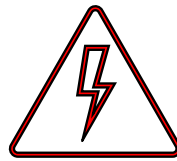
- USB 2.0, Ethernet
- RS485
- Interlock (Enable)
- Analog Input

**Laser Power Control (LPC): [PD option]:**

- CW LPC: Configurable PID
- Start-up phase: Fully parameterizable

<b>Absolute Maximum Ratings</b>	
Supply voltage (DC)	80 V
Supply current (DC)	70 A
Output current	80 A

<b>Operating Ratings</b>	
Temperature	0 – 70 °C
Humidity	5 – 95 %, non-condensing



## **Warning**

**This is a high-power device.**

**Only operate this device in an enclosure.**

**Take necessary precautions to protect the operator.**

## Electrical Characteristics

Unless otherwise noted:  $T_A = 25\text{ °C}$ ,  $U_{IN} = 24\text{ V}$ ,  $R_{load} = 3.3\ \Omega$

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>DC Power Supply Input:</b>						
$U_{IN}$	Supply voltage		18		75	V
$U_{IN}$ Ripple	Ripple tolerance	$U_{IN}$ never below $U_{IN}$ min or above $U_{IN}$ max			300	mV <sub>PP</sub>
<b>Output:</b>						
$I_{OUT}$	Current range		2*		75	A
$U_{OUT}$	Voltage range	$V_{IN} = 75\text{ V}$	0	70		V
$I_{OUT\_RIPPLE}$	Current ripple	$I_{out} > 2\text{ A}$		TBD		mA
$I_{OUT\_RES}$	Current resolution	Driver		1.5		mA
$V_{OUT\_LIMIT}$	Output voltage			TBD		V
$P_{OUT}$	Output power	$V_{LD} = 70\text{ V}$ , additional cooling			5000	W
$f_{CW}$	Current change	For $L_{Load} < 100\text{ nH}$ , higher $f_{CW}$ are possible		TBD		kHz
$I_{OUT\_SLOPE}$	Current slope limit	$I_{out} > 10\text{ A}$		TBD		A/ns
<b>System Characteristics:</b>						
$\eta_{50\%}$	Power efficiency	@ 50 % load		96		%
$\eta_{100\%}$	Power efficiency	@ 100 % load		98		%

\* Operation below this value is possible but the output current and measurement may show offsets outside of the specifications.

## Platform Bus Connector (PBC) X7

Unless otherwise noted:  $T_A = 25\text{ °C}$

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>GPIO 1-8 Characteristics:</b>						
$U_{IH}$	Logic high input threshold		2.35			V
$U_{IL}$	Logic low input threshold				0.9	V
$U_{IMAX}$	Absolute limit input voltage		-0.5		3.6	V
<b>Output Characteristics:</b>						
$U_{OH}$	Logic high output voltage		2.9			V
$U_{OL}$	Logic low output voltage				0.4	V
$R_S$	Series Resistor			100		$\Omega$
<b>Output Characteristics GND:</b>						
$I_{max}$	Absolute limit output current	Total GND Output (Fused)			$\pm 375$	mA

ESD Protection:						
V <sub>PP</sub>	ESD	IEC61000-4-2 Level 4, Contact Discharge		18		kV

### Auxiliary Communication and GPIOs (AUX) X10

Unless otherwise noted: T<sub>A</sub> = 25 °C

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Input Characteristics GPI TTL 1 and 2 (HW v1.20):</b>						
U <sub>IH</sub>	Logic high input threshold		4.7			V
U <sub>IL</sub>	Logic low input threshold				1.8	V
U <sub>IMAX</sub>	Maximum input voltage		-0.5		7	V
<b>Input Characteristics GPI TTL 1 and 2: (HW v1.30 and later):</b>						
U <sub>IH</sub>	Logic high input threshold		3.9			V
U <sub>IL</sub>	Logic low input threshold				1.5	V
U <sub>IMAX</sub>	Maximum input voltage		-0.8		6	V
<b>Input Characteristics Analog In 1V:</b>						
V <sub>AN</sub>	Input voltage range	Analog input	0		1	V
BW	Bandwidth	Analog input		10		kHz
R <sub>IN</sub>	Input Resistance			10		kΩ
<b>Input Characteristics Analog In 5V:</b>						
V <sub>AN</sub>	Input voltage range	Analog input	0		5.3	V
BW	Bandwidth	Analog input		10		kHz
R <sub>IN</sub>	Input Resistance			23		kΩ
<b>Input Characteristics Analog In 10V:</b>						
V <sub>AN</sub>	Input voltage range	Analog input	0		10	V
BW	Bandwidth	Analog input		10		kHz
R <sub>IN</sub>	Input Resistance			37		kΩ
<b>Output Characteristics GND:</b>						
I <sub>max</sub>	Maximum output current	Total GND Output (Fused)	0		±375	mA
<b>Output Characteristics 5V:</b>						
I <sub>max</sub>	Maximum output current	5V Output (Fused)	0		±375	mA
<b>ESD Protection:</b>						
V <sub>PP</sub>	ESD	IEC61000-4-2 Level 4, Contact Discharge		18		kV

### Mini USB Connector X15

Unless otherwise noted:  $T_A = 25\text{ °C}$

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Input Characteristics:</b>						
$V_{IORM}$	Maximum Working Insulation Voltage				560	V

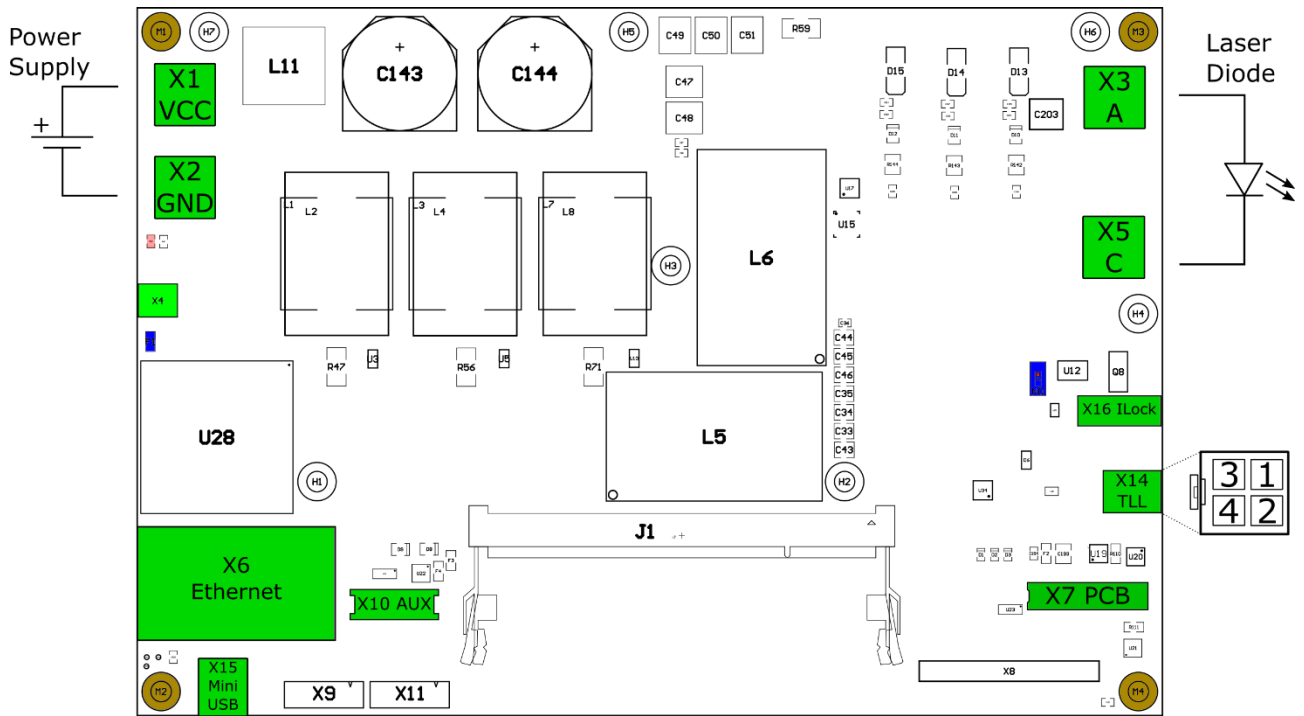
### Interlock Connector X16

Unless otherwise noted:  $T_A = 25\text{ °C}$

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Input Characteristics Interlock:</b>						
$V_{IAct}$	Interlock active input voltage range	Voltage range which is detected as active input	3		30	V
$V_{IORM}$	Maximum Working Insulation Voltage				630	V

## Connectors

Top view



## **Screw Terminals X1, X2, X3 and X5**

M4 Screw terminals

Usable with M4 Ring Connectors or M4 Spade Connectors.

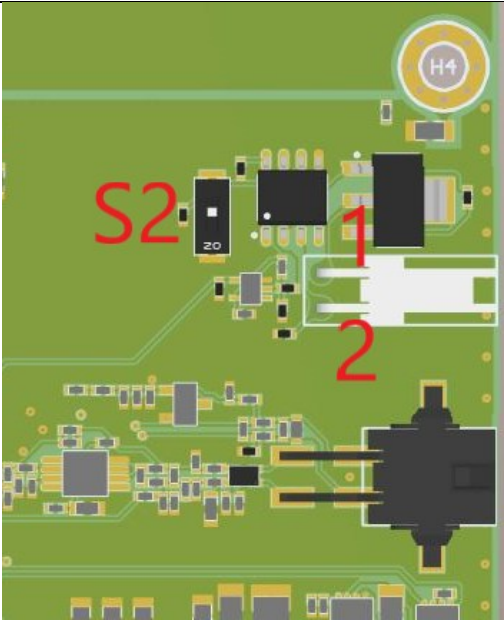
Examples:

TE Connectivity AMP Connectors 171551-1 CONN SPADE TERM 18-22AWG M4 RED

Molex 0193230210 AA1-321-M4T METRIC VERSAK RING T

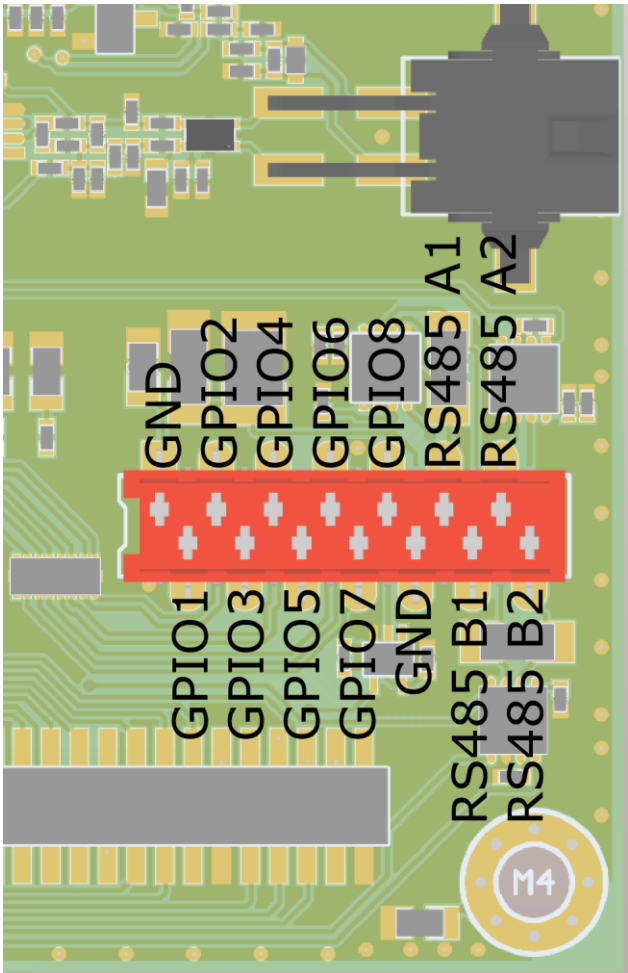
Phoenix Contact 3240023 CONN RING CIRC 14-16AWG M4 CRIMP

## Interlock Connector X16

ILock X16	
 <p>The image shows a top-down view of a green PCB. A circular component labeled 'H4' is in the top right. A DIP-switch labeled 'S2' is in the middle left. Two pins are labeled with red numbers '1' and '2'. Pin 1 is connected to a large black component, and pin 2 is connected to a smaller component. A Molex connector is visible at the bottom right.</p>	<p>To enable the LDD apply a voltage between 3V and 30V between the Interlock + and Interlock – Pins. The Interlock Pins are galvanically isolated from the LDD.</p> <p>The DIP-Switch S2 can be switched ON to override the Interlock functionality.</p> <p>Connector: Molex 0022013027</p>

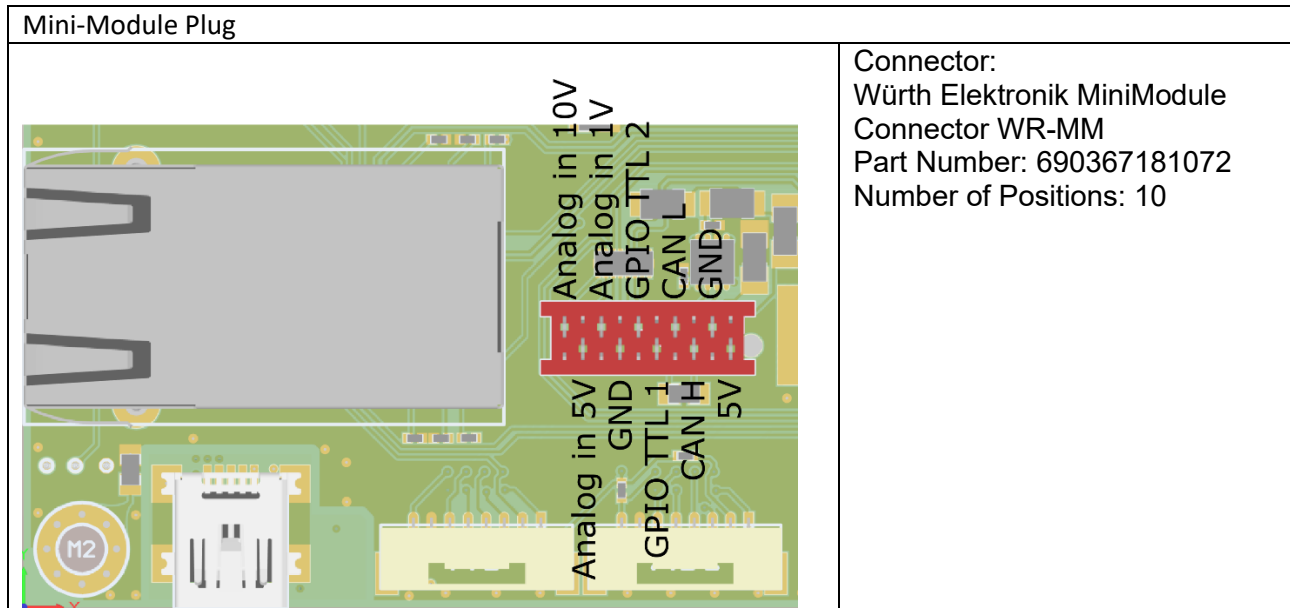
Pin nr.	Name	Description
1	Interlock +	Interlock positive connection
2	Interlock -	Interlock negative connection

## Pin Configuration Platform Bus Connector (PBC) X7

Mini-Module Plug	Connector: MICRO-MATCH SMD FTE Part Number: 8-188275-4 Number of Positions: 14 Centerline (Pitch): 1.27 mm [.05 in]
 <p>The diagram shows a green PCB with a red 14-pin connector. The pins are labeled as follows from top to bottom: GND, GPIO2, GPIO4, GPIO6, GPIO8, RS485 A1, RS485 A2, GPIO1, GPIO3, GPIO5, GPIO7, GND, RS485 B1, and RS485 B2. An M4 screw hole is visible at the bottom right.</p>	

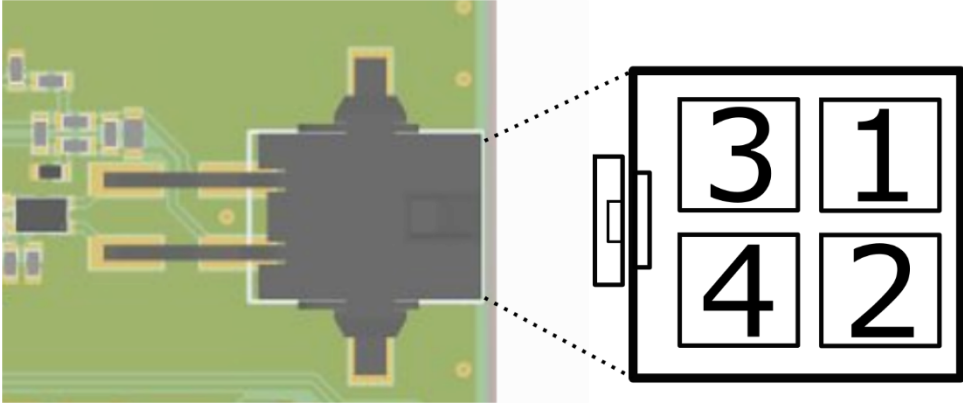
Pin nr.	Name	Description
1	GND EXT	Ground connection
2	GPIO 1	General-purpose input/output Pin 1
3	GPIO 2	General-purpose input/output Pin 2
4	GPIO 3	General-purpose input/output Pin 3
5	GPIO 4	General-purpose input/output Pin 4
6	GPIO 5	General-purpose input/output Pin 5
7	GPIO 6	General-purpose input/output Pin 6
8	GPIO 7	General-purpose input/output Pin 7
9	GPIO 8	General-purpose input/output Pin 8
10	GND EXT	Ground connection
11	RS485 A1	RS485 interface nr. 1A (TX+/RX+ or D+)
12	RS485 B1	RS485 interface nr. 1B (TX-/RX- or D-)
13	RS485 A2	RS485 interface nr. 2A (TX+/RX+ or D+)
14	RS485 B2	RS485 interface nr. 2B (TX-/RX- or D-)

## Pin Configuration Auxiliary Communication and GPIOs (AUX) X10



Pin nr.	Name	Description
1	5V	5V Output
2	GND	Ground connection
3	CANH	CAN High (not available)
4	CANL	CAN Low (not available)
5	GPI TTL 1	General-purpose input with TTL Level
6	GPI TTL 2	General-purpose input with TTL Level
7	GND	Ground connection
8	Analog In 1V	Analog Input 0-1V range
9	Analog In 5V	Analog Input 0-5V range
10	Analog In 10V	Analog Input 0-10V range

## Temperature and Laser Light (TLL) X14

TLL Connector X14	
	<p>Connector TE Connectivity 4- POL Plug Housing 794617-4 With AWG 20-24</p> <p>Crimp Contacts 794610-1</p>

Pin nr.	Name	Description
1	NTC A	NTC Connection
2	NTC B	NTC Connection
3	PD C	Photo Diode Cathode
4	PD A	Photo Diode Anode

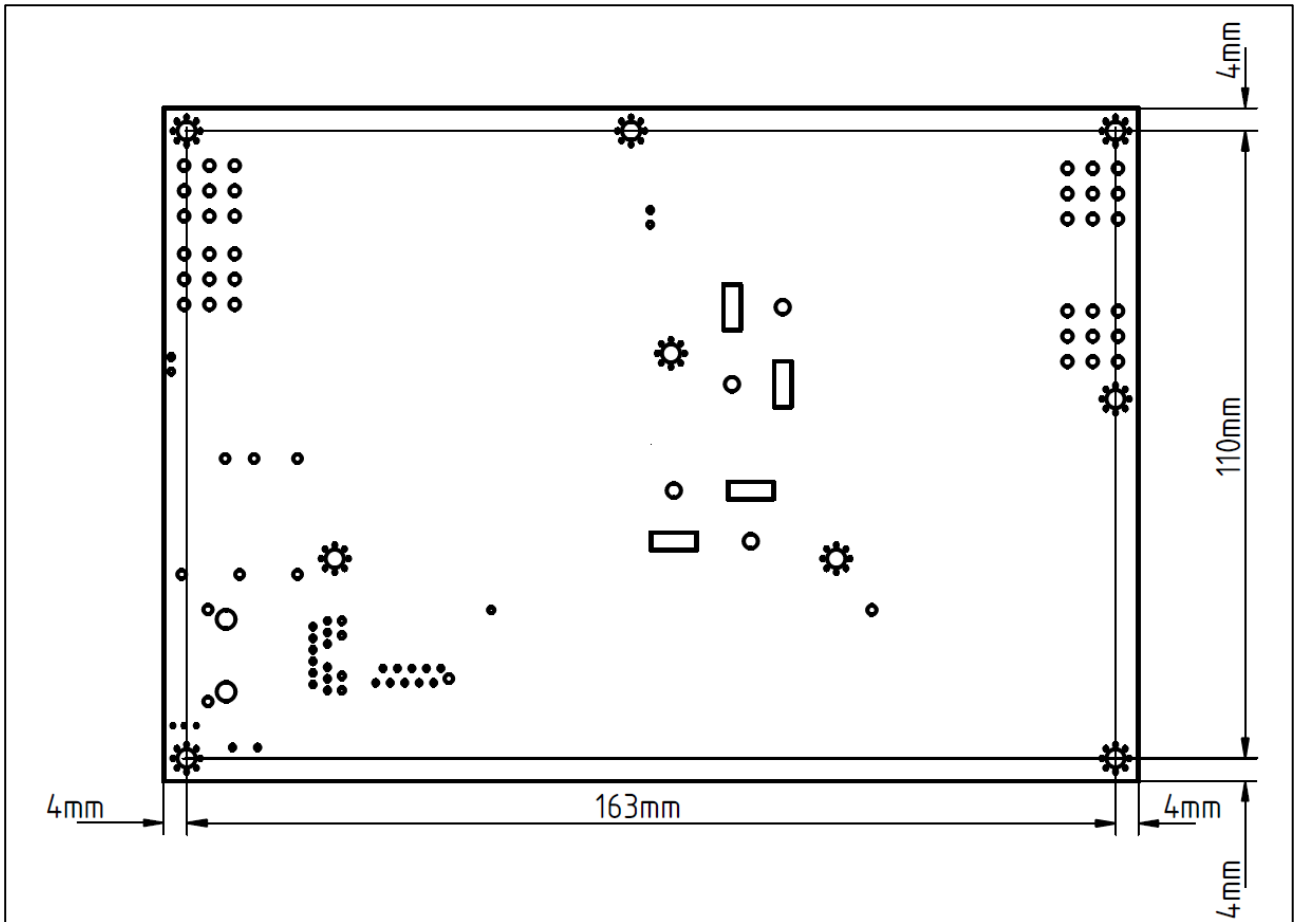
Matching Cable: CAB-6156

### Mini USB Connector X15

The Mini USB Connector X15 can be used to communicate with the LDD using the meCom communications protocol or the Configuration Software. It is electrically isolated.

## Dimensions

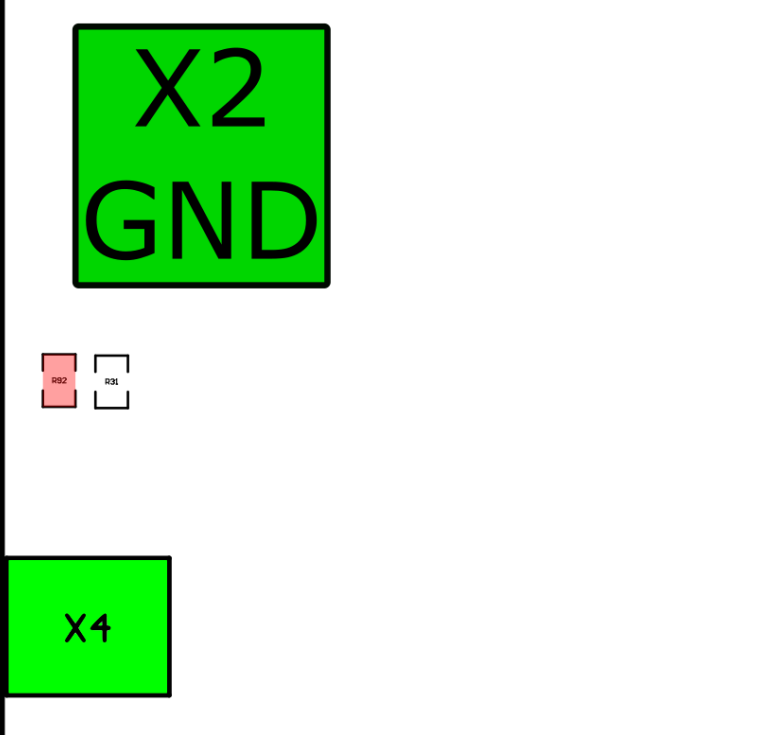
Top View

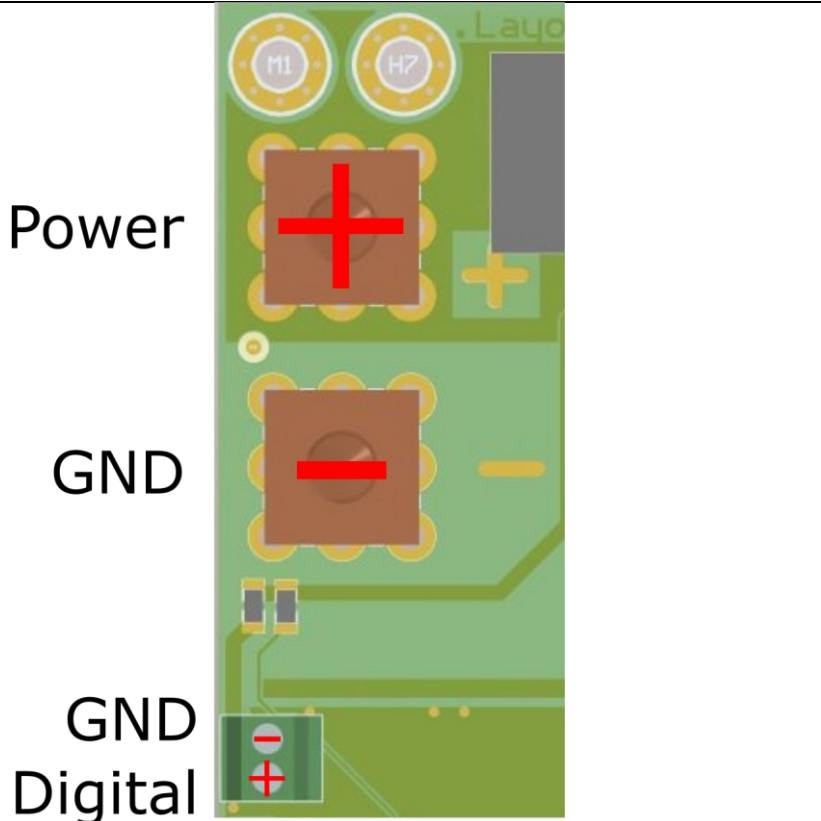


Mounting holes in the corner are 3.2 mm holes for M3 screws.

## Separate supply for Power and Digital parts

The device supports supplying the power and digital sections separately.

Preparation	
	<p>R92 (marked red) must be removed. X4 can then be used to provide power only to the digital part of the LDD.</p> <p>X4 can either be used to directly attach the supply via soldering or to solder a pin header or screw connector.</p>

X4	
	<p>Power requires a Voltage between 18-75V. This supplies the Power electronics of the LDD.</p> <p>Digital supplies the logic part of the LDD and requires 18-75V. The digital part requires &lt;5W of power.</p> <p>GND is shared between both supplies.</p>

### Laser Power -Controlled Operation-Mode [Devices with PD Option only]

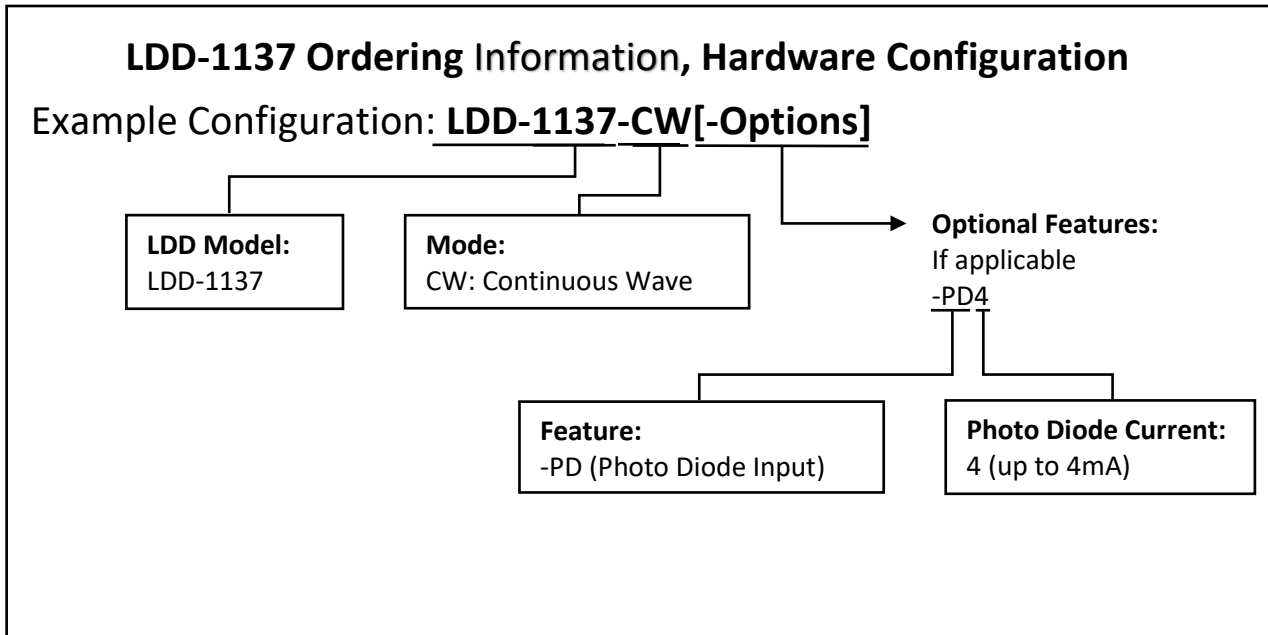
The LDD-1137-PD is a laser power controller that is based on the LDD-1137, with additional light measurement circuitry (photodiode input). A user-defined 'Light System Scale' factor links the generated photocurrent to the absolute light power. The light PID controller's output is fed to the current controller's input. The nominal light power value may be CW, modulated CW (using internal generation) or remotely controlled.

### Light Measurement Characteristics [Devices with PD Option only]

Unless otherwise noted:  $T_A = 25\text{ °C}$

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Photodiode Input Characteristics:</b>						
$I_{PD}$	Photodiode current				4	mA
$f_{ADC\_PD}$	Sampling Frequency	@16bit resolution			1	MSPS

## Ordering Information



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Meerstetter Engineering GmbH (ME) reserves the right to make changes without further notice to the product described herein. Information furnished by ME is believed to be accurate and reliable. However typical parameters can vary depending on the application and actual performance may vary over time. All operating parameters must be validated by the customer under actual application conditions.		

## Change History

Date of change	Doc/Version	Changed / Approved	Change / Reason
4 March 2021	B	PV / MR	<ul style="list-style-type: none"> <li>• Add Change History</li> <li>• First prototype measurement data inserted</li> <li>• New layout template used</li> </ul>
2 December 2021	C	PV / RS	<ul style="list-style-type: none"> <li>• New HW v1.20 changes</li> <li>• High power warning</li> <li>• Measurement plots</li> </ul>
7 September 2022	D	PV / RS	<ul style="list-style-type: none"> <li>• Fixed RS485 polarity</li> <li>• Added X14 pinout</li> <li>• Adjusted TTL levels for HW v1.30</li> </ul>
31 January 2024	E	RS	<ul style="list-style-type: none"> <li>• Update HW version to v1.31</li> <li>• Remove unsupported features</li> </ul>
26 February 2025	F	PV/RS	<ul style="list-style-type: none"> <li>• Added PD order information</li> <li>• Added 1<sup>st</sup> page introduction</li> </ul>
		SC/RS	<ul style="list-style-type: none"> <li>• Refer to HW v1.31 in header</li> <li>• Remove unsupported features</li> </ul>